

RVX7050P

The RVX7050P VCXO combines high frequency, low phase noise (0.5 ps typical, 12 kHz to 20 MHz) and tight frequency stability. This compact SMD (Surface Mount Device) has a 7.0 x 5.0 mm footprint, offering precise frequency stability. It caters to a wide spectrum of applications with a broad selection of industry-standard frequencies, ranging from 8 to 1500 MHz. The RVX7050P boasts a short lead time, ensuring swift project availability.

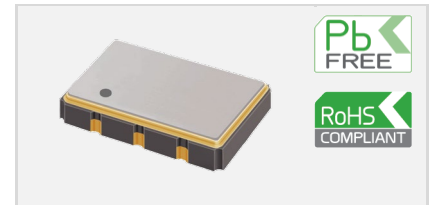
Features

- Fast sample turnaround
- LVC MOS, LVPECL, or LVDS output options
- 0.5 ps typ. RMS phase jitter (12 kHz to 20 MHz)
- Wide frequency range

Applications

- High Speed ADC/DAC/SERDES
- Broadcast Video
- Radio Systems
- DSL/ADSL
- PON/FTTH

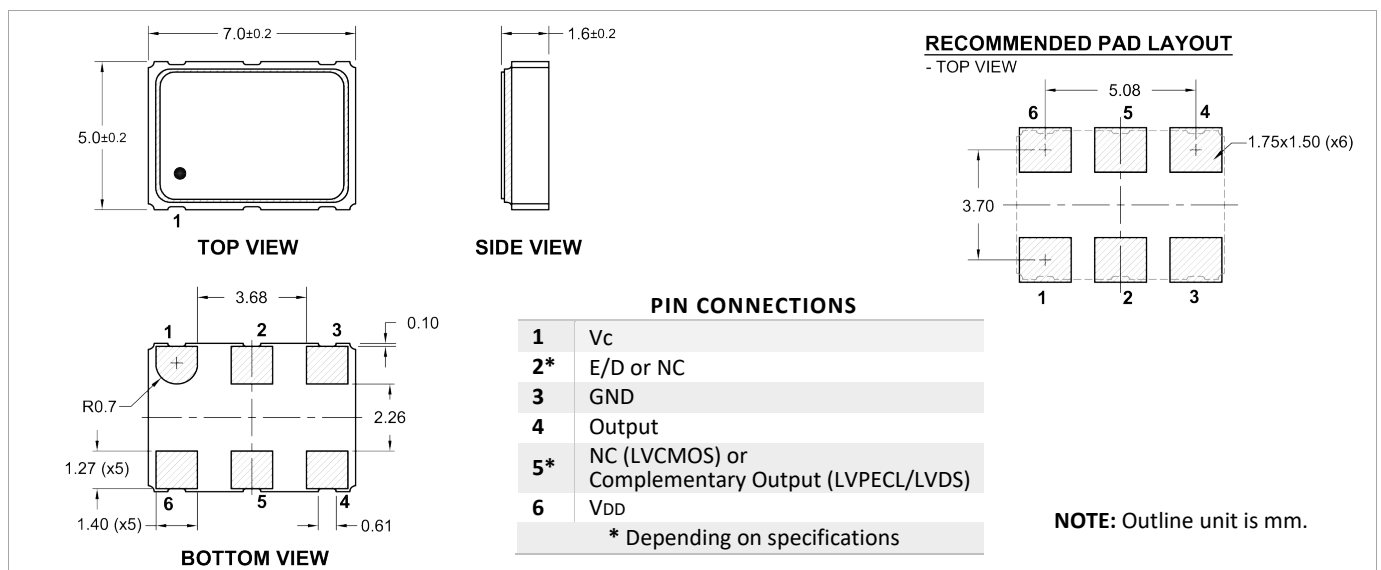
7.0 x 5.0 x 1.6 mm



Standard Specifications

Parameter	Min.	Typ.	Max.	Unit	Test Condition / Description
Nominal frequency	8 8		200 1500	MHz MHz	LVC MOS LVPECL or LVDS
Temperature range	-40		85	°C	-40°C to 105°C is available on request
Temperature stability			±35	ppm	Temperature range: -40 to 85°C
Frequency stability			±50	ppm	Including frequency calibration, operating temperature range, supply and load variations, and 1 year ageing at 25°C. 10 years ageing available on request
Absolute pull range (APR)	±50			ppm	Referenced at V _c = 1.65V
Supply voltage (V _{DD})		2.5 3.3		V	With a tolerance of ±5%
Supply current			30 65 40	mA mA mA	For LVC MOS For LVPECL For LVDS
RMS phase jitter ¹		0.5	1.0	ps	Integrated from 12kHz to 20MHz

Model Outline and Recommended Pad Layout



¹ RMS phase jitter value varies depending on the output type and frequency.