# RXO5032R

The RXO5032R XO combines high frequency, low RMS phase jitter (1.5 ps typical, 12 kHz to 20 MHz) and tight frequency stability. This compact SMD (Surface Mount Device) has a 5.0 x 3.2 mm footprint, offering precise frequency stability. It caters to a wide spectrum of applications with a broad selection of industry-standard frequencies, ranging from 8 to 1500 MHz. The RXO5032R boasts a short lead time, ensuring swift project availability.

## Features

- Fast sample turnaround
- LVCMOS, LVPECL, or LVDS output options
- 1.0 ps typ. RMS phase jitter (12 kHz to 20 MHz)
- Wide frequency range

## Applications

- Ethernet PHY
- Datacentre/Enterprise/SAN
- WLAN, PCIe, Fibre Channel
- DSL/ADSL
- Wi-Fi

## 5.0 x 3.2 x 1.2 mm

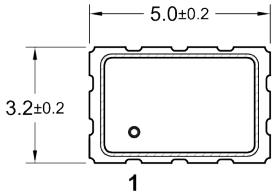
Pb FREE

RoHS COMPLIANT

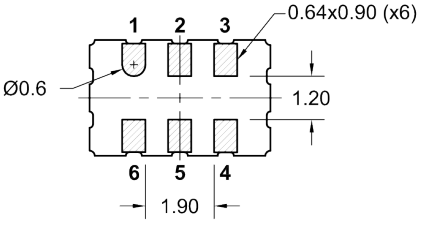
## Standard Specifications

| Parameter | Min. | Typ. | Max. | Unit | Test Condition / Description |
|---|---|---|---|---|---|
| Nominal frequency | 8<br>8 | | 200<br>1500 | MHz<br>MHz | LVCMOS<br>LVPECL or LVDS |
| Temperature range | -40 | | 85 | °C | -40°C to 105°C is available on request |
| Temperature stability | | | ±35 | ppm | Temperature range: -40 to 85°C |
| Frequency stability | | | ±50 | ppm | Including frequency calibration, operating temperature range, supply and load variations, and 1 year ageing at 25°C. 10 years ageing available on request |
| Supply voltage ($V_{DD}$) | | 2.5<br>3.3 | | V | With a tolerance of ±5% |
| Supply current | | | 30<br>65<br>40 | mA<br>mA<br>mA | For LVCMOS<br>For LVPECL<br>For LVDS |
| RMS phase jitter[1] | | 1.0 | 2.0 | ps | Integrated from 12kHz to 20MHz |

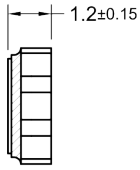
## Model Outline and Recommended Pad Layout




TOP VIEW




BOTTOM VIEW




SIDE VIEW

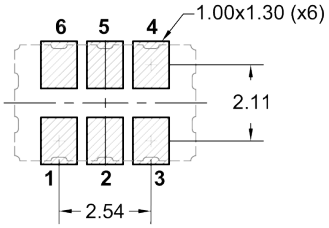
**PIN CONNECTIONS**

| | |
|---|---|
| **1*** | E/D or NC |
| **2*** | E/D or NC |
| **3** | GND |
| **4** | Output |
| **5*** | NC (LVCMOS) or Complementary Output (LVPECL/LVDS) |
| **6** | $V_{DD}$ |

* Depending on specifications

**RECOMMENDED PAD LAYOUT**

- TOP VIEW




**NOTE:** Outline unit is mm.

[1] RMS phase jitter value varies depending on the output type and frequency.